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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/538,013

06/07/2005

Chiori Mochizuki

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FITZPATRICK CELLA HARPER & SCINTO  
30 ROCKEFELLER PLAZA  
NEW YORK, NY 10112

EXAMINER

CRUZ, LESLIE PILAR

ART UNIT

PAPER NUMBER

2826

MAIL DATE

DELIVERY MODE

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PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/538,013	<b>Applicant(s)</b> MOCHIZUKI ET AL.	
	<b>Examiner</b> Leslie P. Cruz	<b>Art Unit</b> 2826	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 02 April 2008.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1,3-7 and 9-23 is/are pending in the application.
- 4a) Of the above claim(s) 13-19 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,3-7,9-12 and 20-23 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 02 April 2008 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                     | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Acknowledgements***

The amendment filed on 02 April 2008 in response to the Office Action mailed on 02 January 2008 has been entered. The present Office Action is made with all the suggested amendments being fully considered. Accordingly, pending in this Office Action are claims 1, 3-7 and 9-23.

### ***Drawings***

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the limitation “a plurality of amplifying thin film transistors” in combination with “the electrode of the sensor element covers each channel region of the plurality of the thin film transistors” must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as “amended.” If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for

consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### ***Claim Rejections - 35 USC § 112***

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 3, 4, 10 and 11 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

Independent claims 1 and 7 recites "an electrode of the sensor element connected to the thin film transistor is disposed above the thin film transistor" and "the thin film transistors are connected in series with one another and use a same gate wiring". Claim 3 and 10 recites "the plurality of thin film transistors comprise: a plurality of transferring thin film transistors for transferring electrical signals from the sensor

elements, respectively; and a plurality of resetting thin film transistors for resetting the sensor elements, respectively". It is unclear how the plurality of the transferring thin film transistors and the plurality of resetting thin film transistors are connected in series with one another and use a same gate wiring.

Claims 4 and 11 recites "the plurality of thin film transistors comprise: a plurality of amplifying thin film transistors for receiving as their inputs electrical signals from the sensor **elements**, respectively; a plurality of transferring thin film transistors for outputting the electrical signals, respectively; and a plurality of resetting thin film transistors for resetting the sensor elements, respectively". It is unclear if the applicants meant "sensor element" or "sensor elements" and how the plurality of amplifying thin film transistors, plurality of transferring thin film transistors and plurality of resetting thin film transistors are connected in series with one another and use a same gate wiring.

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1, 3-7 and 9-23 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Independent claims 1 and 7 recites "an electrode of the sensor element connected to the thin film transistor is disposed above the thin film transistor" and "the thin film transistors are connected in series with one another and use a same gate

wiring". It is unclear if the applicant meant the sensor element is connected to the plurality of thin film transistors or one of the plurality of thin film transistors.

Claim 3 and 10 recites "the plurality of thin film transistors comprise: a plurality of transferring thin film transistors for transferring electrical signals from the sensor elements, respectively; and a plurality of resetting thin film transistors for resetting the sensor elements, respectively". It is unclear how the plurality of the transferring thin film transistors and the plurality of resetting thin film transistors are connected in series with one another and use a same gate wiring.

Claims 4 and 11 recites "the plurality of thin film transistors comprise: a plurality of amplifying thin film transistors for receiving as their inputs electrical signals from the sensor **elements**, respectively; a plurality of transferring thin film transistors for outputting the electrical signals, respectively; and a plurality of resetting thin film transistors for resetting the sensor elements, respectively". It is unclear if the applicants meant "sensor element" or "sensor elements" and how the plurality of amplifying thin film transistors, plurality of transferring thin film transistors and plurality of resetting thin film transistors are connected in series with one another and use a same gate wiring.

### ***Response to Arguments***

Applicant's arguments with respect to claims 1 and 7 have been considered but are moot in view of the new ground(s) of rejection.

***Telephone/Fax Information***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leslie P. Cruz whose telephone number is 571-272-8599. The examiner can normally be reached on Monday-Friday 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sue A. Purvis can be reached on 571-272-1236. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Leslie Pilar Cruz  
Examiner  
Art Unit 2826

/Minh-Loan T. Tran/  
Primary Examiner  
Art Unit 2826